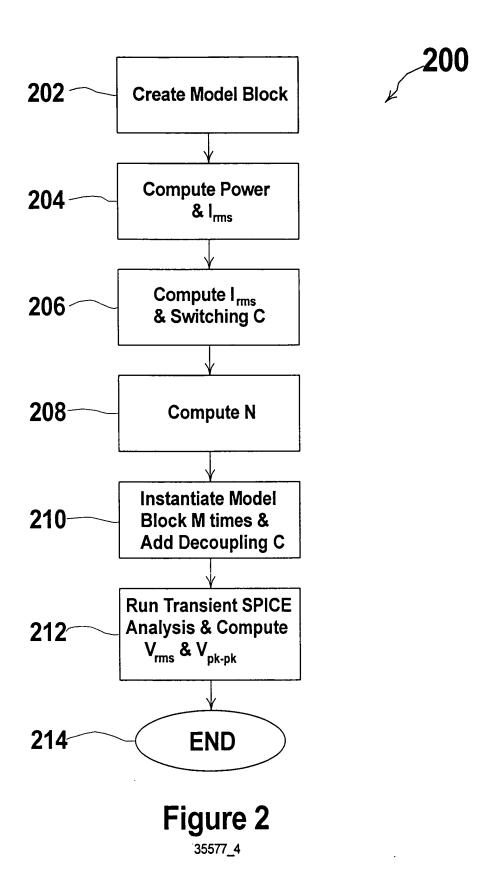


Figure 1 35577_1



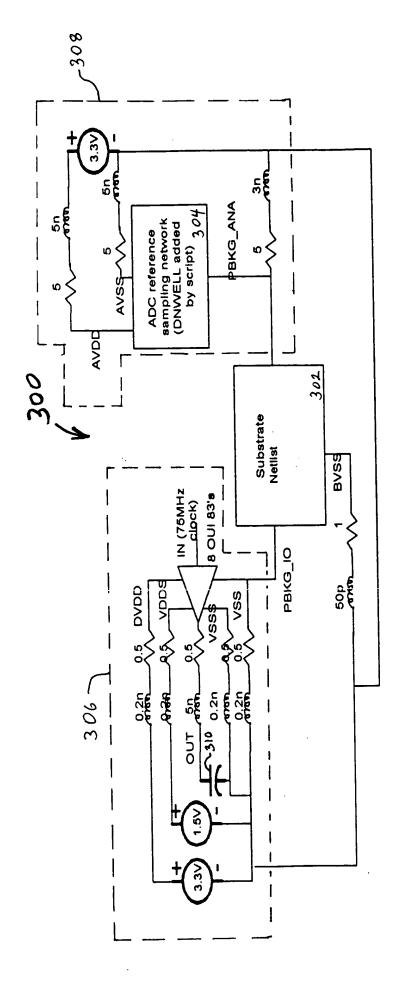


Figure 3

MIPS ISO PW 514	DSP ISO PW	<u>510</u>
MIPS DNW <u>512</u>	DSP DNW	<u>508</u>
LF ISO PW		<u>506</u>
LF DNW		<u>504</u>
518 518 REC (PSUB, NW, ISO)		<u>502</u>
		518

Figure 5 35577_2

